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EXAMINER WILLER HOLD

ART UNIT PAPER NUMBER

2814

DATE MAILED: 63/25/99

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No. Applicant(s) 08/866,129

Examiner

Uemura et al.

Douglas Wille

Group Art Unit 2814



<u> </u>
ormal matters, prosecution as to the merits is closed C.D. 11; 453 O.G. 213.
xpire <u>3</u> month(s), or thirty days, whicheve respond within the period for response will cause the sof time may be obtained under the provisions of
is/are pending in the application.
is/are withdrawn from consideration
is/are allowed.
is/are rejected.
is/are objected to.
are subject to restriction or election requirement.
eview, PTO-948. to by the Examiner. isapproveddisapproved. der 35 U.S.C. § 119(a)-(d). te priority documents have been er) ternational Bureau (PCT Rule 17.2(a)).

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

Application/Control Number: 08/866,129 Page 2

Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 102

- 1. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 2. Claims 12 14 and 21 are rejected under 35 U.S.C. 102(e) as being anticipated by Nakamura et al. (*422)
- 3. With respect to claims 12 14, Nakamura et al.('422) show a group III compound semiconductor device (see Figure 1) with a p-type upper layer 13 and an electrode consisting of a layer of Ni with a layer of Au on top (column 5, line 49). Figure 7 shows a modification of the Figure 1 device which has a contact layer 15 and a bonding pad 17 that covers part of layer 15 and has a protective film of silicon oxide (column 10, line 26). The other properties in claim 12 are inherent in the materials.
- 4. With respect to claim 21, Nakamura ('422) shows a structure with a AuNi layer covering part of a Ni and Au layer and will inherently have the same properties as claimed.

Claim Rejections - 35 USC § 103

5. Claims 1 - 11, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Nakamura et al. (*422) in view of Manabe et al. and Nakamura et al. (*350).

Application/Control Number: 08/866,129 Page 3

Art Unit: 2814

- Nakamura et al. ('422) show a group III compound semiconductor device (see Figure 1) 6 with a p-type upper layer 13 and an electrode consisting of a layer of Ni with a layer of Au on top (column 5, line 49). Figure 7 shows a modification of the Figure 1 device which has a contact layer 15 and a bonding pad 17 that covers part of layer 15 and has a protective film of silicon oxide (column 10, line 26). Nakamura et al. ('422) also show that the bonding pad 17 is composed of Ni and Au but teach against the use of Al (in a two layer structure) since it can migrate to the electrode and can degrade it. Manabe et al. show the use of Al in a multilayer electrode stack (see Figure 6 and column 5, line 38) which has improved operating characteristics. It would have been obvious to modify the Nakamura et al. ('422) device to include the Al layer as taught by Manabe et al with the expectation that the two intervening layers will protect the electrode from deterioration. Nakamura et al. ('422) also teach annealing at 600 degrees (column 7, line 38) and teach the LED compound is In_xAl_yGa_{1-x-y}N. Nakamura et al. ('350) show that the silicon oxide protective layer is SiO₂ (column 34, line 66). The remainder of the claimed features are inherent in the choice of materials. Forming the layers in the sequence Ni-Au-Al follows the decreasing sequence of work functions and would also be obvious.
- 7. With respect to claim 20, Nakamura ('422) shows a structure with a AuNi layer covering part of a Ni and Au layer and will inherently have the same properties as claimed.

Response to Arguments

8. Applicant's arguments filed 1/19/99 have been fully considered but they are not persuasive

Application/Control Number: 08/866,129 Page 4

Art Unit: 2814

Applicant argues that neither Nakamura references shows a three layer structure, which is true, but Manabe is relied upon to teach the third layer. Applicant also attempts to distinguish between an electrode pad and an electrode with the difference depending only upon the relative placement of the layers, thus calling the lower NiAu layer the electrode and calling the upper AuNi layers the pad. However, the claim is addressed to the three layer structure and this structure is shown in the prior art quoted. Note also that Nakamura ('422) shows a separate layer on top of the two layer structure.

Applicant states that none of the references show the Ni Au reversal but note that Nakamura ('422) shows the layer sequence of Ni and Au with a partial covering of NiAu and further shows an annealing at greater than 400 degrees C (column 5, line 65). Thus with identical structure and identical processing, identical results will be obtained.

Conclusions

- Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).
- 12. A shortened statutory period for response to this final action is set to expire THREE MONTHS from the date of this action. In the event a first response is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event will the statutory period for response expire later than SIX MONTHS from the date of this final action.

- Any inquiry concerning this communication or earlier communications from the examiner should be directed to Douglas A. Wille whose telephone number is (703) 308-4949.
- 14. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose number is (703) 308-0956.

Olik Chaudhuri

Supervisory Patent Examiner

Art Unit 2814

DAW A. Lee.

March 17, 1999